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REMARKS

Applicant notes with appreciation the detailed response provided by Examiner in the instant office action. Applicant has amended Claims 1, 5, 10, and 15, cancelled Claims 2-3, and added new dependent Claims 18-21. No new matter is introduced by these amendments, and a new search is not required. Applicant respectfully requests reconsideration in view of the amendments and the following remarks.

Claims 1-17 were rejected under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 5,811,837 to Misawa et al. (hereinafter *Misawa*) in view of U.S. Patent No. 6,198,307 to Garlepp et al. (hereinafter *Garlepp*).

Independent Claim 1 is amended to recite the subject matter of previous dependent Claims 2-3, and specifically to recite "the second signal wire being directly connected to the ground through a resistor, the first clock signal being generated in the timing controller, the first signal wire and the second signal wire being formed in parallel" (emphasis added) which is neither taught nor fairly suggested by *Misawa* and *Garlepp* in any combination.

Applicant respectfully asserts that the structure of the second signal wire connected directly to ground through a resistor, and the first signal wire and the second signal wire being formed in parallel, is not taught by the prior art. Instead, *Misawa* teaches a liquid crystal device having a unit

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cell pitch that is twice the picture element pitch, and includes an internal driver circuit comprising thin film transistors having the same cross-section structure as the switching elements (Misawa col. 2 lines 9-16 and FIG. 1-4D). In contrast, Garlepp teaches an external driver circuit providing an output impedance that closely matches the loaded impedance of the signal line at all times in order to minimize secondary reflections on the signal line, and optional embodiments where the signal line is coupled to ground through a capacitor (Garlepp col. 2 line 56 to col. 3 line 19, and FIGS. 7-9, emphasis added). Further, Garlepp teaches a differential driver 15 and a pair of differential clock lines 17 comprising conductors 18 and 19, where the ends of conductors 18 and 19 are tied together through terminating resistors R_{T1} and R_{T2} to form an electrical connection (Garlepp col. 7 lines 12-53 and FIG. 9).

Independent Claim 1, as amended, recites a structure including "second signal wire being directly connected to the ground", in a direct current (DC) connection, while Garlepp teaches an alternating current (AC) connection to ground through a capacitor (Garlepp col. 11 lines 53-57 and FIG. 9). Applicant respectfully asserts that the differential driver, clock lines, and terminating resistors tying the clock lines together do not teach the timing controller, the separate clock lines, or the direct termination of the anti-phase clock

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to ground in order to reduce electromagnetic interference (EMI) as in the presently claimed invention. Respectfully, while both *Misawa* and *Garlepp* teach electrical circuits, the application and structure of their respective circuits, as well as the results of their disclosed structures do not provide a teaching towards combination, and even if combined as suggested they do not teach all the elements of the presently claimed invention. Further, while *Misawa* teaches light reflection from surfaces (381, 382), *Misawa* does not teach the problem of signal reflection in clock lines, or the need for addressing such an issue by combination with *Garlepp*. Finally, neither *Misawa* nor *Garlepp* teach the problem or need for a solution related to electromagnetic interference (EMI).

Similarly, independent Claim 10, as amended, recites "the second shift clock signal wire being directly connected to a ground through a resistor, the first shift clock signal wire and the second shift clock signal wire being formed in parallel" which is neither taught nor fairly suggested for at least the reasons provided with respect to independent Claim 1. In particular, independent Claim 10, as amended, recites a structure including "second shift clock signal wire being directly connected to a ground through a resistor", in a direct current (DC) connection, while *Garlepp* teaches an alternating current (AC) connection to ground through a capacitor (*Garlepp* col. 11 lines 53-57 and FIG. 9).

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Therefore, Applicant respectfully submits that independent Claims 1 and 10 are patently distinguished from *Misawa* in view of *Garlepp*, and dependent Claims 4-9 and 11-17 are also distinguished at least on this basis. Therefore, Applicants respectfully request the rejection of Claims 1-17 under 35 U.S.C. 103(a) be withdrawn.

New dependent Claims 18-19 depend from independent Claim 1, and are supported by the application as filed (Specification page 10 lines 16-20, page 11 lines 2-5, and FIG. 2). Applicant respectfully asserts these new claims also distinguish over *Misawa* in view of *Garlepp* at least based upon their dependence from independent Claim 1, and that no new search is required. Similarly, new dependent claims 20-21 depend from independent Claim 10, and are supported by the application as filed (Specification page 10 lines 16-20, page 11 lines 2-5, and FIG. 2). Applicant respectfully asserts these new claims also distinguish over *Misawa* in view of *Garlepp* at least based upon their dependence from independent Claim 10, and that no new search is required.

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In view of the above amendments and remarks, Applicant respectfully requests the withdrawal of the rejection, and timely issuance of both an advisory action and/or a notice of allowance. If the Examiner has any questions or concerns, a telephone call to the undersigned at (949) 752-7040 is both welcomed and encouraged.


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I hereby certify that this paper is being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.


Monique M. Butler

November 9, 2006
Date of Signature

Respectfully submitted,


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